IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

:

Averett, Guy E. et al. : Examining Group: 2811

:

Serial No.: 10/072,145

Filed: 02/07/2002 : Examiner: Ori Nadav

For: SEMICONDUCTOR DEVICE AND METHOD OF PROVIDING REGIONS OF LOW SUBSTRATE CAPACITANCE

AMENDMENT

Honorable Commissioner of Patents, P.O. BOX 1450 Alexandria, VA 22313-1450

SIR:

In response to the Office Action mailed July 9, 2007, please reconsider the above-identified application in view of the amendments and remarks presented hereinafter.